Test Problems

a. **Overview of 8051 micro controller architecture**
   Q1. How Harvard architecture is implemented in 8051
   Q2. List the features of 8051
   Q3. What are the alternate functions of Port 3, port 2, and Port 0
   Q4. List all SRF’s
   Q5. How do you differentiate between SFR bits and internal memory bits?
   Q6. What are the advantages of register banks?
   Q7. What is the maximum program memory and data memory can be interfaced externally
   Q8. List all interrupt vectors.

b. **Overview of 8096 micro controller architecture**
   Q1. What is RALU architecture in 8096?
   Q2. List all the features of 8096
   Q3. What is reset location of 8096?
   Q4. Explain HSI and HSO in 8096
   Q5. How DAC is realized using PWM output of 8096
   Q6. Explain three operands instruction in 8096
   Q7. Explain watch dog timer in 8096
   Q8. Explain ADC in 8096
   Q9. What is the maximum baud rate possible in 8096?
   Q10. Bring out the differences between T1 and T2 timers in 8096.