Assignment 6

Due on: 2020-03-11, 22:59 IST.

Unit 8 - Week 6

Course outline
- How does an FPGA work?
- How does an FPGA work?
- How to access the course?

Week 1
Week 2
Week 3
Week 4
Week 5
Week 6

Week 7
Week 8

Assignment 6

1. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)

2. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)

3. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)

4. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)

5. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)

6. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)

7. Consider the following circuit consisting of a single inverter gate. (F). Design a circuit consisting of a single inverter gate. (F)