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**NPTEL (<https://swayam.gov.in/explorer?ncCode=NPTEL>) » Architectural Design of Digital Integrated Circuits (course)**

Announcements ([announcements](#))

**About the Course ([https://swayam.gov.in/nd1\\_noc20\\_ee37/preview](https://swayam.gov.in/nd1_noc20_ee37/preview))** [Ask a Question \(forum\)](#)

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**Due on 2020-02-12, 23:59 IST**

1. Given Only Two Xor Gates One Must Function As Buffer And Another As Inverter?
2. What Is Difference Between Latch And Flip-flop?
3. Difference Between Mealy And Moore State Machine?
4. How to Implement An And Gate Using Mux?
5. Compare And Contrast Synchronous And Asynchronous Reset?
6. What Is A Johnson Counter?

## Your Submission:

The due date for submitting this assignment has passed.  
As per our records you have not submitted this assignment.

