

Unit 8 - Week 6

Course outline

How does an NPTEL online course work?

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Assignment 6

The due date for submitting this assignment has passed. **Due on 2020-03-11, 23:59 IST.**
 As per our records you have not submitted this assignment.

- Which of the following instruction of 8051 is not permitted?

 - a) ADD A,R4
 - b) MOV R0,A
 - c) MOV R3,R2
 - d) MOV R5,DPL

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: c.
- Which of the following is a valid immediate addressing mode?

 - a) MOV A,R1
 - b) MOV 45h,A
 - c) MOV A, #42h
 - d) MOV DPTR,#68975h

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: c.
- Which of the following instruction is equivalent to **MOV A,#55h**

 - a) MOV 0F0H,#55H
 - b) MOV 0E0H,#55H
 - c) MOV 0E0H,55H
 - d) MOV 0F0H,55H

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: b.
- Identify the operation performed by the following program

```
CLR A
MOV R1,#60H
MOV R7,#16
AGAIN: MOV @R1,A
INC R1
DJNZ R7,AGAIN
```

 - a) Clear 16 ROM locations starting at ROM address 60H
 - b) Clear 16 RAM locations starting at RAM address 60H
 - c) Increment the contents of memory locations (60H-6FH) by one
 - d) Increment the contents of memory locations (60H-75H) by one

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: b.
- Which of the following RAM locations are bit addressable

 - a) 20H-2FH
 - b) 00H-0FH
 - c) 70H-7FH
 - d) 10H-1FH

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: a.
- What is the result after execution of **SETB P1.5**

 - a) Bit 5 of Register b is set to 1
 - b) Bit 5 of port P1 is set to 0
 - c) Bit 5 of port P1 is set to 1
 - d) Bit 1 of port P5 is set to 1

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: c.
- What is the contents of the accumulator after execution of following instructions

```
mov a,#54h
swap a
RL a
```

 - a) 6DH
 - b) A8H
 - c) ABH
 - d) 8AH

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: d.
- Which of the following statement is true after execution of **MUL AB?**

 - a) High-order byte of the result stored in B register when overflow flag is set to 1
 - b) carry flag is cleared to 0
 - c) Lower-order byte of the result stored in A
 - d) All of the above

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: d.
- Identify the contents of A and B after the following instructions executed.

```
MOV A,#2Fh
MOV 0F0h,#12h
DIV AB
DIV AB
```

 - a) A = 02h, B = 0Bh
 - b) A = 00h, B = 02h
 - c) A = 02h, B = 00h
 - d) A = 0Bh, B = 02h

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: b.
- Indicate which mode and which timer are selected for the following instruction.

```
MOV TMOD,#20H
```

 - a) mode 2 of timer 1 is selected
 - b) mode 2 of timer 0 is selected
 - c) mode 1 of timer 1 is selected
 - d) mode 1 of timer 0 is selected

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: a.
- Which of the following statement is true, if timer operates in mode 2?

 - a) It is an 16-bit timer
 - b) When the TL register overflows, TL is reloaded automatically with the original value kept by the TH register
 - c) It is an 13 bit timer
 - d) When the TL register overflows, TL register loaded with 00h

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: b.
- Contents of the register A after execution of following instructions

```
MOV A, #47h
MOV B, #25h
ADD A,B
DA A
```

 - a) A = 6Ch
 - b) A = 72h
 - c) A = A8h
 - d) A = 62h

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: b.
- Which of the following is not a valid 8051 instruction?

 - a) INC DTIPR
 - b) INC A
 - c) DEC DTIPR
 - d) DEC A

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: c.
- Which of the following flags are affected by the instruction **DIV AB** of an 8051 microcontroller?

 - a) OV,CY,AC
 - b) OV,AC
 - c) OV,CY
 - d) CY,AC

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: c.
- What is the content of the memory location specified R0 after the following set of instructions are executed?

```
MOV @R0, #04H
MOV A, #11H
XCHD A, @R0
```

 - a) 01H
 - b) 10H
 - c) 04H
 - d) 40H

a.
 b.
 c.
 d.

No, the answer is incorrect.
 Score: 0
 Accepted Answers: a.