

## Unit 14 - Week 12

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## Assignment 12

The due date for submitting this assignment has passed.  
As per our records you have not submitted this assignment.

**Due on 2020-04-22, 23:59 IST.**

- 1) Find the output current ( $I_{out}$ ) of DAC0808(Digital to analog converter) for following conditions:  
For binary input  $(10100000)_2$  and  $I_{ref} = 5\text{mA}$
- $78.4\ \mu\text{A}$
  - $3.125\ \mu\text{A}$
  - $3.125\ \text{mA}$
  - $7.84\ \text{mA}$
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
c.
- 2) Which pin decides the 8086 mode of operation?
- $\overline{MN}/\overline{MX}$
  - $\overline{DT}/\overline{R}$
  - ALE
  - $\overline{BHE}/S_7$
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
a.
- 3) In 8086, the address bus is \_\_\_\_\_ bits wide.
- 16
  - 8
  - 20
  - 32
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
c.
- 4) Consider the following features of a CPU. Match them with 8086 or 8088 processor.
- 16-bit data bus
  - 8-bit data bus
  - BHE signal
  - 20-bit address bus
- (i) – 8086, (ii) – 8088, (iii) – 8088, (iv) – 8086
  - (i) – 8086, (ii) – 8088, (iii) – 8086, (iv) – 8088
  - (i) – 8088, (ii) – 8086, (iii) – 8088, (iv) – 8086
  - (i) – 8086, (ii) – 8088, (iii) – 8086, (iv) – 8086
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
d.
- 5) Which register used as loop counter in 8086 microprocessors?
- AX
  - BX
  - CX
  - DX
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
c.
- 6) Which of the following is not present in Bus Interface Unit?
- Instruction queue
  - Segment registers
  - Instruction pointer
  - Flag register
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
d.
- 7) Calculate the physical address for Segment offset pair 7213: 5686.
- 0C899h
  - 777B6h
  - 776B6h
  - 777B9h
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
b.
- 8) Which of the following address lines in an 8086 microprocessor are multiplexed with status signals
- $A_3 - A_0$
  - $A_{19} - A_{16}$
  - $A_{15} - A_0$
  - $A_8 - A_0$
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
b.
- 9) Instruction Queue size of 8086 microprocessor?
- 4 bytes
  - 6 bytes
  - 10 bytes
  - 2 bytes
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
b.
- 10) Co-processor can be attached with 8086 in
- Minimum mode
  - Maximum mode
  - Both Minimum and Maximum modes
  - No mode
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
b.
- 11) The action performed by the following instruction of 8086:  
 $\text{MOV}[1234\text{h}], \text{AX}$  is?
- Move contents of memory location 1234h to register AX
  - Move the contents of register AX to memory offset 1234h
  - Add contents at 1234h and AX
  - Add contents of 1234h and AX and store the result in 1234h
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
b.
- 12) The WAIT instruction of 8086 checks the pin
- TEST
  - BHE
  - DEN
  - HOLD
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
a.
- 13) In MOVS instruction of 8086, the segment register for the destination is
- CS
  - DS
  - ES
  - SS
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
c.
- 14) The ALU in EU of 8086 is
- 16 bit
  - 20 bit
  - 8 bit
  - 24 bit
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
a.
- 15) In 8086, data access by which register uses SS as the default segment register
- SP
  - BP
  - Both SP and BP
  - DI
- a.  
 b.  
 c.  
 d.
- No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
c.