

Unit 13 - Week 11

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Assignment 11

The due date for submitting this assignment has passed.
As per our records you have not submitted this assignment.

Due on 2020-04-15, 23:59 IST.

- 1) Find the contents of control word of the 8255 for the following configuration:
All the ports of A, B and C are input ports (mode 1)
- 3Fh
 - BFh
 - 9Fh
 - 1Fh
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
b.
- 2) In 8255, bit addressability is available with port
- A
 - B
 - C
 - D
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 3) Which of the following representations related to the AVR microcontrollers is correct?
- Timer/Counter 0 is an 8-bit up counter and timer/counter 1 is an 8-bit up counter
 - Timer/Counter 0 is an 8-bit up counter and timer/counter 1 is a 16-bit up counter
 - Timer/Counter 0 is a 8-bit up counter and timer/counter 1 is an 16-bit down counter
 - Timer/Counter 0 is a 16-bit up counter and timer/counter 1 is a 16-bit up counter
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
b.
- 4) Which of the following representations related to the AVR microcontrollers is correct?
- An AVR timer/counter 0 has dual output compares A and B
 - An AVR timer/counter 1 has a single output compare A
 - An AVR timer/counter 1 has a single output compare B
 - An AVR timer/counter 1 has dual output compares A and B
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
d.
- 5) Which of the following represents the best description of an AVR timer/counter source when CS02 = 0, CS01 = 1, and CS00 = 1?
- Stop, the timer/counter is stopped
 - CPU frequency is divided by 8 (CK/8)
 - CPU frequency is divided by 64 (CK/64)
 - CPU frequency is divided by 256 (CK/256)
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 6) Which of the following registers holds the counter value of timer/counter 0 in an AVR microcontroller?
- TCCR0
 - TCNT0
 - TCCR1A
 - TCNT1
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
b.
- 7) In an AVR microcontroller, interrupts are used for
- RESET
 - Timers and Time-Critical Code
 - Hardware Signalling
 - All of the above
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
d.
- 8) Why interface chips are necessary in a microcontroller based system?
- To resolve the speed problem
 - To synchronize the data transfers between the CPU and the I/O device
 - To synchronize the data transfers between the CPU and the I/O device, as well as to resolve the speed problem
 - None of the given options
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 9) Which of the following can be associated with bouncing contacts?
- Push-button switches
 - Toggle switches
 - Both push-button switches and toggle switches
 - Neither push-button switches nor toggle switches
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 10) Which of the following is a hardware solution to suppress a switch bounce in a microcontroller based system?
- An RC time constant whose value is smaller than the switch bounce
 - An LC time constant whose value is smaller than the switch bounce
 - An RC time constant whose value is larger than the switch bounce
 - An LC time constant whose value is larger than the switch bounce
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 11) The term DIP in the phrase 'DIP switch' stands for
- Double In-lane Package
 - Double In-line Package
 - Dual In-line Package
 - Dual In-lane Package
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 12) Find the control word of the 8255 for resetting BIT 4 of Port C.
- 00001111
 - 10001111
 - 10001000
 - 00001000
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
d.
- 13) To get 2 displayed by a 7 Segment display(common anode), what should be the combination of 8051 pins (P1.7 – P1.0)?
- 01111101
 - 10111110
 - 01011011
 - 10100100
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 14) Which of the following is the correct combination to select Port C in an 8255 chip?
- CS = 0, A1 = 0, A0 = 0
 - CS = 0, A1 = 0, A0 = 1
 - CS = 0, A1 = 1, A0 = 0
 - CS = 1, A1 = 0, A0 = 0
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
c.
- 15) With respect to ADC 0804, find out which of the following statement(s) are false.
- CS is active high pin.
 - D7-D0 are output pins.
 - The Chip has a voltage range of 0 - 5 V.
- Option 1.
 - Option 2.
 - Option 3.
 - All the options.
- a.
 b.
 c.
 d.
- No, the answer is incorrect.
Score: 0
Accepted Answers:
a.