Assignment 6

The due date for submitting this assignment has passed.
As per our records you have not submitted this assignment.

1) The number of 8-bit memory required to construct 32kb-memory are:
   - 4
   - 8
   - 12
   - 16
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   0

2) Upon RESET, the 8086 microprocessor starts the operations from the memory location
   - 0000 H
   - 0FFFF H
   - 22FF H
   - FF00 H
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   FF00 H

3) If the starting address of 1 KB memory is 00500 H, then the ending address would be
   - 005FF H
   - 00CF H
   - 01400 H
   - 01500 H
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   01500 H

   - 00000 H-FFFFF H
   - F0000 H-FFFFF H
   - FE000 H-FFFFF H
   - FF000 H-FFFFF H
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   FE000 H-FFFFF H

5) If the chip select of 255 is CS = A14.A13.A12.A11. The 8-bit address of control word register would be
   - 02 H
   - 01 H
   - 00 H
   - FF H
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   02 H

6) In order to program Port A and Port B as output ports in Mode 0 and Port C as input port in Mode 0, the control word for 8255 would be
   - 03 H
   - 02 H
   - 01 H
   - FF H
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   03 H

7) The time taken to execute MOV CX, FF00H using 8086 with 5 MHz clock would be
   - 0.3 µs
   - 0.4 µs
   - 0.6 µs
   - 0.9 µs
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   0.3 µs

8) The number of clock cycles required to execute NOP operation of 8086 is
   - 1
   - 2
   - 3
   - 4
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   2