Assignment 4

The due date for submitting this assignment has passed. As per your records you have not submitted this assignment.

1) After the execution of CALL instruction
   a. The contents of stack pointer will be incremented by 1
   b. The contents of stack pointer will be decremented by 1
   c. The contents of stack pointer will be incremented by 2
   d. The contents of stack pointer will be decremented by 2
   Score: 0
   Accepted Answers:
   The contents of stack pointer will be decremented by 2
   No, the answer is incorrect.

2) The instructions REPZ CMPSB compare string bytes
   a. Until end of the string or until string bytes not equal
   b. Until end of the string or until string bytes equal
   c. Until end of the string or until string bytes not equal
   d. Until end of the string or until string words not equal
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   Until end of the string or until string bytes not equal
   No, the answer is incorrect.

3) Type 3 interrupt is
   a. Divide by zero interrupt
   b. Single step interrupt
   c. Breakpoint interrupt
   d. Interrupt on overflow
   Score: 0
   Accepted Answers:
   Breakpoint interrupt
   No, the answer is incorrect.

4) The size of interrupt pointer table in 8086 is
   a. 256 bytes
   b. 128 bytes
   c. 1024 bytes
   d. 512 bytes
   Score: 0
   Accepted Answers:
   1024 bytes
   No, the answer is incorrect.

5) After the execution of INT 2 instruction
   a. The contents of stack pointer will be incremented by 2
   b. The contents of stack pointer will be decremented by 2
   c. The contents of stack pointer will be incremented by 4
   d. The contents of stack pointer will be decremented by 4
   Score: 0
   Accepted Answers:
   The contents of stack pointer will be decremented by 4
   No, the answer is incorrect.

6) Which of the following instruction is used to pass the instructions from 8086 to microprocessor 8087
   a. INT 3
   b. JMP
   c. LOCK
   d. ESC
   Score: 0
   Accepted Answers:
   ESC
   No, the answer is incorrect.

7) The execution of WAIT instruction causes 8086 to enter into idle state until
   a. The TEST pin is made LDR
   b. The TEST pin is made HIGH
   c. The LOCK pin is made LDR
   d. The LOCK pin is made HIGH
   Score: 0
   Accepted Answers:
   The TEST pin is made LDR
   No, the answer is incorrect.

8) If [DI]=0000H and [BP]=FF00H, after the execution of the instruction MOV, the contents of DI and AX respectively are
   a. 0000H and FFFFH
   b. FFFFH and 0000H
   c. FFFFH and 0000H
   d. FFFFH and FFFFH
   Score: 0
   Accepted Answers:
   FFFFH and 0000H