Assignment 12

The due date for submitting this assignment has passed.
As per our records you have not submitted this assignment.

1) If the status of 8251A control signals are CD = 1, RD = 0 and WR = 1. Then which of the following operation will be performed?
   - Microprocessor writes instructions in the control register
   - Microprocessor reads the status from the status register
   - Microprocessor outputs data to data buffer
   - Microprocessor accepts the data from data buffer
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   Microprocessor reads the status from the status register

2) Which of the following 8251A signal is not MODEM control signal?
   -Ready
   -DSR
   -RTS
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   RTS

3) If 82h is loaded into 8255A mode word register, which of the following is correct?
   - 8250A operates in synchronous mode
   - 8251A operates in asynchronous mode with baud rate factor of 1
   - 8251A operates in asynchronous mode with baud rate factor of 16
   - 8251A operates in asynchronous mode with baud rate factor of 64
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   8250A operates in synchronous mode

4) If the port address of 8251A status register is FFH, the port address of control register would be
   - FCH
   - FCH
   - FEH
   - FFH
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   FFH

5) If the transmitter clock frequency of 8251A is 153.6kHz and the baud rate in asynchronous mode is 2400, the baud rate factor would be
   - 1
   - 15
   - 32
   - 64
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   64

6) The size of display random access memory in 8729 is
   - 8 x 8
   - 15 x 8
   - 32 x 8
   - 64 x 8
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   8 x 8

7) The size of FIFO/Serial RAM in 8729 is
   - 8 x 8
   - 16 x 8
   - 32 x 8
   - 64 x 8
   
   No, the answer is incorrect.
   Score: 0
   Accepted Answers:
   8 x 8