Unit 11 - Week 9: I/O Interfacing and Timer

Assignment 9

The due date for submitting this assignment has passed. As per our records you have not submitted this assignment.

1) If the address of 8254 control word register is 0F8H, the address of counter 0 would be
   - 0F8H
   - 0F9H
   - 0FAH
   - 0FBH
   No, the answer is incorrect. Score: 0
   Accepted Answers: 0FAH

2) If 8F4H is loaded into control word register of 8254, then the mode selected would be
   - Mode 2
   - Mode 3
   - Mode 4
   - Mode 5
   No, the answer is incorrect. Score: 0
   Accepted Answers: Mode 3

3) If mode 0 of 8254, the GATE input must be
   - kept LOW
   - kept HIGH
   - changed from LOW to HIGH
   - changed from HIGH to LOW
   No, the answer is incorrect. Score: 0
   Accepted Answers: kept LOW

4) Which of the following mode generates square wave?
   - Mode 2
   - Mode 3
   - Mode 4
   - Mode 5
   No, the answer is incorrect. Score: 0
   Accepted Answers: Mode 4

5) In order to read the count in the 8254 counter while count is still going on, which of the following operation would be performed?
   - Latching
   - Triggering
   - Sourcing
   - Single stepping
   No, the answer is incorrect. Score: 0
   Accepted Answers: Latching

6) If the chip select of 8254 is generated using $CS = A_0 \overline{A_1} A_2 A_3$ , the address of counter 2 would be
   - 04H
   - 05H
   - 06H
   - 07H
   No, the answer is incorrect. Score: 0
   Accepted Answers: 05H

7) In order to generate 1 KHz square wave using 8254 operated at 2 MHz clock, the event that must be loaded into counter would be
   - 0FFH
   - 78H
   - 100H
   - 200H
   No, the answer is incorrect. Score: 0
   Accepted Answers: 200H

8) The hardware-triggered strobe mode of 8254 is same as software-triggered strobe mode except that
   - Triggering is performed by rising pulse at GATE
   - Triggering is performed by falling pulse at GATE
   - Triggering is performed by rising pulse at Clock
   - Triggering is performed by falling pulse at Clock
   No, the answer is incorrect. Score: 0
   Accepted Answers: Triggering is performed by rising pulse at GATE