Assignment 8

The due date for submitting this assignment has passed.
As per our records you have not submitted this assignment.

Due on: 2020-11-11, 23:59 IST

1) What will happen when there is a brown-out condition?

- Only POR
- Only PUC
- First POR then PUC
- First PUC then POR

No, the answer is incorrect.
Score: 0
Accepted Answers:
First POR then PUC

2) Which of the following clock sources are not present in MSP4308256?

- DOO
- CLK
- LXT
- XT2

No, the answer is incorrect.
Score: 0
Accepted Answers:
DOO

3) What is the frequency range of DOO in MSP4308256?

- 32Hz to 16kHz
- 32kHz to 16kHz
- 32kHz to 16MHz
- 32kHz to 256kHz

No, the answer is incorrect.
Score: 0
Accepted Answers:
due to reset

4) What is the frequency range of DOO in MSP4308256?

- 32Hz to 16kHz
- 32kHz to 16kHz
- 32kHz to 16MHz
- 32kHz to 256kHz

No, the answer is incorrect.
Score: 0
Accepted Answers:
due to reset

5) What is the average number of instructions per second on the MSP430 microcontroller if it is operating a program at 1MHz clock frequency?

- 2.66 MIPS
- 4.5 MIPS
- 1 MIPS
- None of the above

No, the answer is incorrect.
Score: 0
Accepted Answers:
2.66 MIPS

6) Which of the following flags when '1' represents generation of an – Non maskable interrupt?

- ACC[6]
- TRIFG
- OISFG
- VBST[6]

No, the answer is incorrect.
Score: 0
Accepted Answers:
ACC[6]

7) Global Interrupt Enable bit can disable Non Maskable Interrupt. The statement is

- True
- False

No, the answer is incorrect.
Score: 0
Accepted Answers:
False

8) What are contents of the main program should be stored on Stack before servicing an Interrupt subroutine?

- Program Counter only
- Contents of flags register only
- Program Counter and Status Register
- None of the above

No, the answer is incorrect.
Score: 0
Accepted Answers:
Program Counter and Status Register

9) Which Interrupt source has the highest priority and lowest priority in MSP430 Microcontroller?

- RESET
- PORT1
- TMR1
- None of the above

No, the answer is incorrect.
Score: 0
Accepted Answers:
Program Counter and Status Register

10) What is the correct sequence for execution of an Interrupt subroutine?

- PC and Stack contents of the main program are stored on Stack.
- SP is cleared.
- Contents of stack are restored into PC and SR.
- Hard interrupt flag is cleared.
- Interrupt subroutine code is executed.
- Interrupt with highest priority is selected and associated subroutine is selected.

- 1-2-3-4-5-6
- 1-4-5-6-3-2
- 1-3-2-4-5-6
- 2-1-3-4-5-6

No, the answer is incorrect.
Score: 0
Accepted Answers:
1-2-3-4-5-6