Assignment 4

Due on 2020-02-26, 20:59 IST.

1. The 8-bit 0101-0110 instruction is used to determine
   - The instruction is A Arithmetic or C instruction
   - The A register value from a register or flag register
   - The A register value from a flag register or memory
   - The A register is in a flag register

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - If the instruction is A Instruction or C Instruction
   - If the A register uses value from a register or flag register
   - If the A register uses value from a flag register or memory
   - If the A register is in a flag register

   2. The bit 0 in 0111 0111 0111, which is present in the jump bit of 0112

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - If the instruction is A Instruction or C Instruction
   - The bit 0 digital (1-2, 1-2, 1-2), which are present in the jump bit of 0112

   3. Which HACK instructions will give 16-bit HACK instruction translate to CP?

   a) 1011 0010 1000 1000

   b) 0011 0100 0000

   c) 1110 1000 0000

   d) All of the above

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 1011 0010 1000 1000
   - 1110 1000 0000
   - None of the above

   4. Which of the following instructions can be generated by HACK IN? Let

   a) ADD
   b) DCM
   c) MVI
   d) NOP

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - ADD
   - DCM
   - MVI
   - NOP

   5. Which of the following words COINCISE about HACK CPU?

   a) Only write operations are supported for instruction memory.
   b) Both read and write operations are supported for data memory.
   c) Only read operation is supported for instruction memory.
   d) Both read and write operations are supported for instruction memory.

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - Only read operation is supported for instruction memory.
   - Both read and write operations are supported for instruction memory.

   6. Which of the following words is NOT valid HACK assembly instructions?

   a) ADD
   b) DCM
   c) MVI
   d) NOP

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - ADD
   - DCM
   - MVI

   7. The following boolean function is given an input to load start of PC. Which of the following statements is COMMITED?

   a) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   b) 11 = 0001, 10 = 1100, 01 = 1101, 00 = 1101

   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   d) 11 = 0001, 10 = 1100, 01 = 1101, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1100, 01 = 1101, 00 = 1101

   8. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   9. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   10. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   d) All of the above

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   11. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   12. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   13. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   14. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   15. If the bit 0 is true for JST if bit 0

   a) The boolean function for load is still correct.
   b) The boolean function for load is still correct.
   c) 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101

   a) The answer is incorrect.
   b) No answer.

   Accepted Answers:
   - 11 = 0001, 10 = 1101, 01 = 1100, 00 = 1101