Assignment 2

The due date for submitting this assignment has passed. Dues on 2019-02-13, 23:59 IST.

As per our records you have not submitted this assignment.

1) The decimal number \((35.5)_{10}\) in binary notation is:

- \(100011.101\)
- \(100011.1\)
- \(00110101.0101\)
- \(110101.101\)

No, the answer is incorrect.
Score: 0
Accepted Answers:
- \(b\)

2) The number \((110.101001)_{2}\) in hexadecimal notation is:

- \(6.A4\)
- \(C.A4\)
- \(6.29\)
- \(C.29\)

No, the answer is incorrect.
Score: 0
Accepted Answers:
- \(a\)

3) Using \(n\) bits the maximum and minimum numbers that can be represented in 2's complement notation are:

- \(2^n - 1\) and \(-2^n - 1\)
- \(2^n - 1\) and \(-2^n\)
- \(2^n - 1\) and \(-2^{n+1}\)
- \(2^n - 1\) and \(-2^n + 1\)

No, the answer is incorrect.
Score: 0
Accepted Answers:
- \(a\)
4) The addressing mode that adds the displacement and the index register to get the effective address of the operand is called:

a. Indexed addressing
b. Base-Indexed addressing
c. Register Indirect addressing
d. Relative addressing

No, the answer is incorrect.
Score: 0
Accepted Answers: a

5) Registers R1 and R2 contain values 300 and 1200 respectively in decimal, and the word length of the processor is 32 bits. The effective address of the memory operand in decimal for the instruction “STORE R5, 70 (R1, R2)” will be __________.

No, the answer is incorrect.
Score: 0
Accepted Answers: (Type: Numeric) 1570

6) Which of the following statement is false?

a. RISC is costlier than CISC
b. RISC is faster than CISC
c. RISC is a load-store architecture
d. CISC has more number of addressing modes than RISC

No, the answer is incorrect.
Score: 0
Accepted Answers: a
For a computer based on 3-address instruction formats, each address field is used to specify which of the following?

(S1) A memory operand  
(S2) A processor register  
(S3) An immediate constant data

a. Either S1 or S2  
b. Either S2 or S3  
c. Only S2 and S3  
d. All of S1, S2 and S3

No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
a  
b  
c  
d

Which of the following statements are false for MIPS32 register set?

a. Register R0 always contains the value 0.  
b. Any register other than R0 can be used for register indirect addressing.  
c. Any register other than R0 can be used to hold the return address for function calls.  
d. R31 is a special register used as stack pointer.

No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
c  
d

In MIPS instruction `BLT $S1,$S2,Label` means:

a. If $S1$ is less than $S2$ then do not branch to location $Label$  
b. If $S1$ is not less $S2$ then branch to location $Label$  
c. If $S2$ is less than $S1$ then branch to location $Label$  
d. If $S1$ is less than $S2$ then branch to location $Label$

No, the answer is incorrect.  
Score: 0  
Accepted Answers:  
d
In MIPS32, to fetch a 32-bit word from memory in a single cycle, the word has to be stored from memory location:

a. 0018H
b. 0019H
c. 001AH
d. 001BH

No, the answer is incorrect.
Score: 0
Accepted Answers: a