Week 0: Assignment 0

The due date for submitting this assignment has passed.

As per our records you have not submitted this assignment.

1) Choose the correct option: S1: C-Based VLSI Design is about starting VLSI design from C-code, S2: Electronic design automation tool helps designer to develop design at higher abstraction level and then automatically synthesizes the design into lower abstraction level.
   
   - [ ] Both S1 and S2 are correct
   - [ ] Only S1 is correct
   - [ ] Only S2 is correct
   - [ ] Both S1 and S2 are not correct

   No, the answer is incorrect.  
   Score: 0  
   Accepted Answers:  
   - [ ] Both S1 and S2 are correct

2) Consider the following combinational circuit. Consider all gates has equal delay, 1 unit. What would be the delay of the overall circuit?

   ![Combinational Circuit Diagram]

   - [ ] 1 unit
   - [ ] 2 units
   - [ ] 3 units
   - [ ] 4 units

   No, the answer is incorrect.  
   Score: 0  
   Accepted Answers:  
   - [ ] 3 units

3) What is the latency of the following sequential circuit?

   ![Sequential Circuit Diagram]

   - [ ] 1
   - [ ] 2
   - [ ] 3
   - [ ] 4

   No, the answer is incorrect.  
   Score: 0  
   Accepted Answers:  
   - [ ] 3

4) Select the correct option. S1: C-Based VLSI design will reduce the overall design time S2: C-Based VLSI design will increase the design complexity since the design will now be considered at higher abstraction level such as C/C++.

   - [ ] Only S1 is correct
   - [ ] Only S2 is correct
   - [ ] Both S1 and S2 are not correct
   - [ ] Both S1 and S2 are correct

   No, the answer is incorrect.  
   Score: 0  
   Accepted Answers:  
   - [ ] Only S1 is correct

5) This course helps us to understand

   - [ ] How C code is transformed to Register transfer level design automatically
   - [ ] How to write C code to synthesize efficient hardware
   - [ ] How to synthesize an efficient hardware from a given C code.
   - [ ] All of the above

   No, the answer is incorrect.  
   Score: 0  
   Accepted Answers:  
   - [ ] All of the above

6) This course will also help us to understand

   - [ ] Verification of C-Based VLSI design process
   - [ ] How to synthesize secure design from C
   - [ ] How to use the optimizations of the synthesis tool effectively to develop efficient hardware from C
   - [ ] All of the above

   No, the answer is incorrect.  
   Score: 0  
   Accepted Answers:  
   - [ ] All of the above