



Unit 6 - Week 1 Assignment 1

Course outline

How to access the portal

Week1 - Lec-1: Introduction to Parallel Computing Architectures and Programming Models

Week1 : Lec-2: Parallelism in Single Processor Architectures

Week1: Lec-3: Parallel Architectures

Week 1 Assignment 1

Quiz : Assignment 1

Week2: Lec-4: First OpenMP program and Threads

Assignment 1

The due date for submitting this assignment has passed.
As per our records you have not submitted this assignment.

Due on 2019-09-11, 23:59 IST.

1) A cache can be beneficial even if the same data is not accessed again **1 point**

- True
 False

No, the answer is incorrect.
Score: 0

Accepted Answers:
True

2) In the shared memory programming model, each processing thread has its own local address space **1 point**

- True
 False

No, the answer is incorrect.
Score: 0

Accepted Answers: