Assignment 6

Due on 2020-03-11, 23:59 IST.

1. What type of stage is used to create a micro-substitute?
   - Group III element
   - Group IV element
   - Group V element
   - Uterium
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: Group III element

2. A basic S-R flip-flop consists of cross-coupled:
   - AND or OR gates
   - SCR or MOS flip-flops
   - NOR or NAND flip-flops
   - AND or OR flip-flops
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: NOR or NAND flip-flops

3. For a positive-edge triggered D flip-flop, the outputs reflect the input condition when:
   - The clock pulses are LOW
   - The clock pulses are HIGH
   - The clock pulses transitions from LOW to HIGH
   - The clock pulses transitions from HIGH to LOW
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: The clock pulses transition from LOW to HIGH

4. Semiconductors are made up of:
   - On or Y1
   - Si or P1
   - Ge or Pb
   - None of the above
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: On or Y1

5. A 2 input NOR gate consists of:
   - 3 transistors
   - 3 transistors
   - 4 transistors
   - None of the above
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: 3 transistors

6. An input decoder has _______ outputs.
   - 0
   - 2
   - 4
   - 8
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: 2

7. A basic SRAM cell consists of _______ transistors.
   - 0
   - 2
   - 4
   - None of the above
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: 0

8. How many select lines would be required for an 8-to-1 multiplexer?
   - 2
   - 4
   - 8
   - 16
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: 2

9. A basic decoder is constructed from _______.
   - Inverters
   - Inverters and AND gates
   - AND gates
   - None of the above
   - No, the answer is incorrect.
   Score: 0
   Accepted Answer: Inverters and AND gates

10. A ROM that consists of:
    - 2 input bits in a 1-to-1 multiplexer
    - 4 input bits in a 1-to-1 multiplexer
    - 8 input bits in a 1-to-1 multiplexer
    - None of the above
    - No, the answer is incorrect.
    Score: 0
    Accepted Answer: 2 input bits in a 1-to-1 multiplexer