Assignment 5

The due date for submitting this assignment has passed.
Due on 2020-03-04, 23:59 IST.

1. The x86_GIA indicates that the FP registers in the FP machine model of x86_GIA -

   - are overlapping set of registers
   - Communicate directly
   - Communicate by any memory
   - None of these.
   
   No, the answer is incorrect. Score: 0
   Accepted Answers: Communicate only by memory.

2. FP immediate in x86_GIA is entered by

   - storing its free representation in the memory and using fall instruction
   - storing its free representation in integer registers and using fall instruction
   - storing its free representation in the memory and using fall instruction
   - storing its free representation in integer registers and using fall instruction.

   No, the answer is incorrect. Score: 0
   Accepted Answers: Storing its free representation in the memory and using fall instruction.

3. Which of the following is TRUE for x86_GIA

   - The instruction accesses the base of x86_GIA memory.
   - The two instruction exchanges the contents of two floating point registers.
   - The instruction converts the FP value to an integer before saving it to memory.
   - None of these.

   No, the answer is incorrect. Score: 0
   Accepted Answers: None of the above.

4. Which of the following is a special instruction for computing-

   - Absolute value
   - Square root
   - Trigonometric expressions
   - All of the above.

   No, the answer is incorrect. Score: 0
   Accepted Answers: All of the above.

5. Which of the following is a single-address x86 instruction in x86_GIA?

   - add
   - jmp
   - inc
   - mov

   No, the answer is incorrect. Score: 0
   Accepted Answers: mov.

6. The x86_GIA indicates that register indirect addressing mode with no displacement-

   - register indirect addressing mode with no displacement
   - register indirect addressing mode with 1 byte displacement
   - register indirect addressing mode with 4 byte displacement
   - None of these.

   No, the answer is incorrect. Score: 0
   Accepted Answers: None of the above.

7. The suffix "f" which added to instruction of the x86_GIA usually indicates that this-

   - Instruction addressably some flag
   - Instruction addressably some flag
   - Instruction addressably some flag
   - Instruction addressably some flag.

   No, the answer is incorrect. Score: 0
   Accepted Answers: Instruction addressably some flag.

8. x86 stands for-

   - Special index byte
   - Special index byte
   - Double Index Byte
   - Doubt Index Byte.

   No, the answer is incorrect. Score: 0
   Accepted Answers: Double Index Byte.

9. The size of a dword (double word) is-

   - 16 bits
   - 8 bits
   - 4 bytes
   - Both a and b.

   No, the answer is incorrect. Score: 0
   Accepted Answers: Both a and b.