

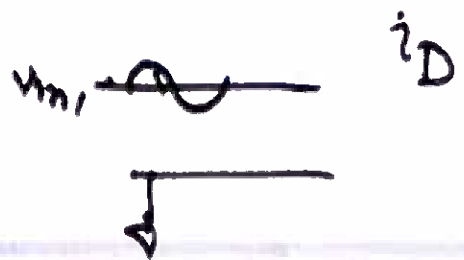
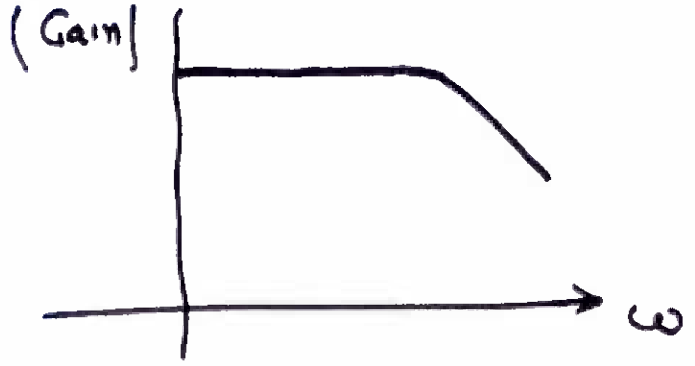
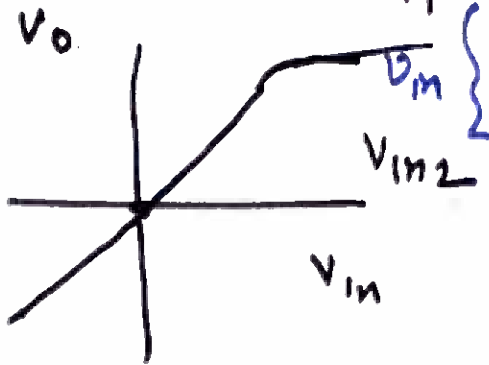
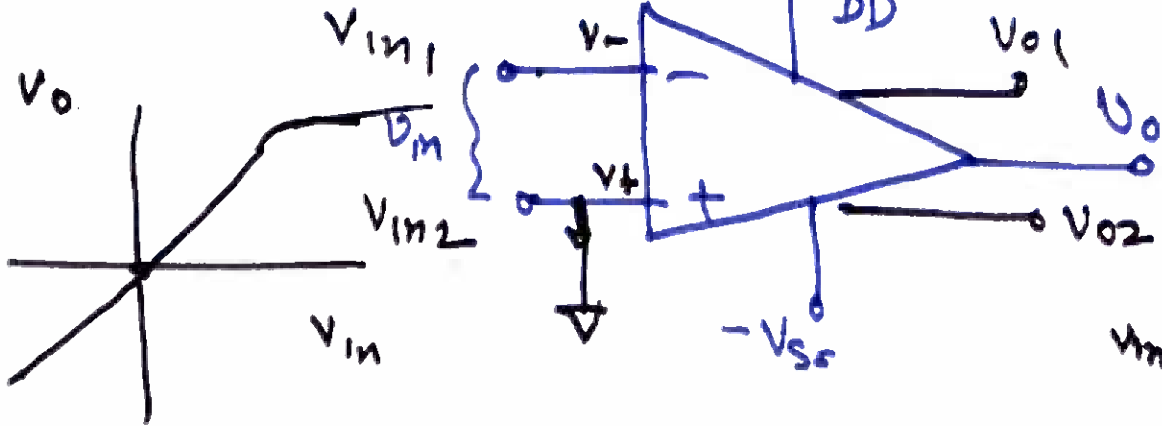
# Slow Rate

## OPAMP

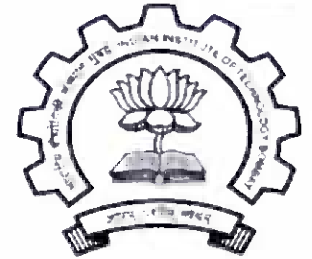
$$C_L \frac{dV_o}{dt} = \frac{I_{SS}}{C_L}$$



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## Differential Amplifier



1. MOS DIFFAMP

2. BJT DIFFAMP

Differential Amplifier has two inputs  $V_{in1}$ , &  $V_{in2}$ .

This gives :—

$$\text{Difference signal } V_{id} = V_{in1} - V_{in2}$$

$$\text{and Common Mode Signal } V_{cm} = \frac{V_{in1} + V_{in2}}{2}$$

} Input

Major advantages of Diffamp is much Higher Immunity to environmental Noise, Higher Voltage Swings, with disadvantage of additional Area in an IC.

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The Minimum Occures , such that  $V_S$  which is Source Voltage  $M1$  &  $M2$ , has value enough for creating Current Source  $I_{sc}$

$$V_{DS} \text{ for } C_{Source} = V_S - (-V_{SS})$$

$$\therefore V_{CM_{MIN}} = V_{GS} + V_T + V_{CS} - V_T - V_{SS}$$

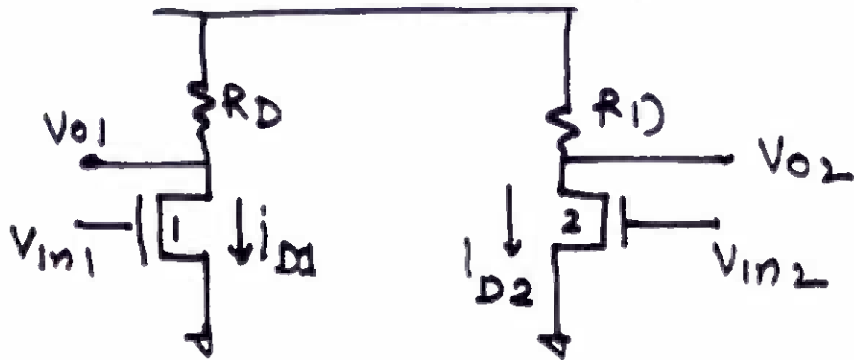
$$V_{CM_{MIN}} = V_{OV} + V_T + V_{CS} - V_{SS}$$

Range  $ICMR = V_{C_{max}} - V_{C_{min}}$

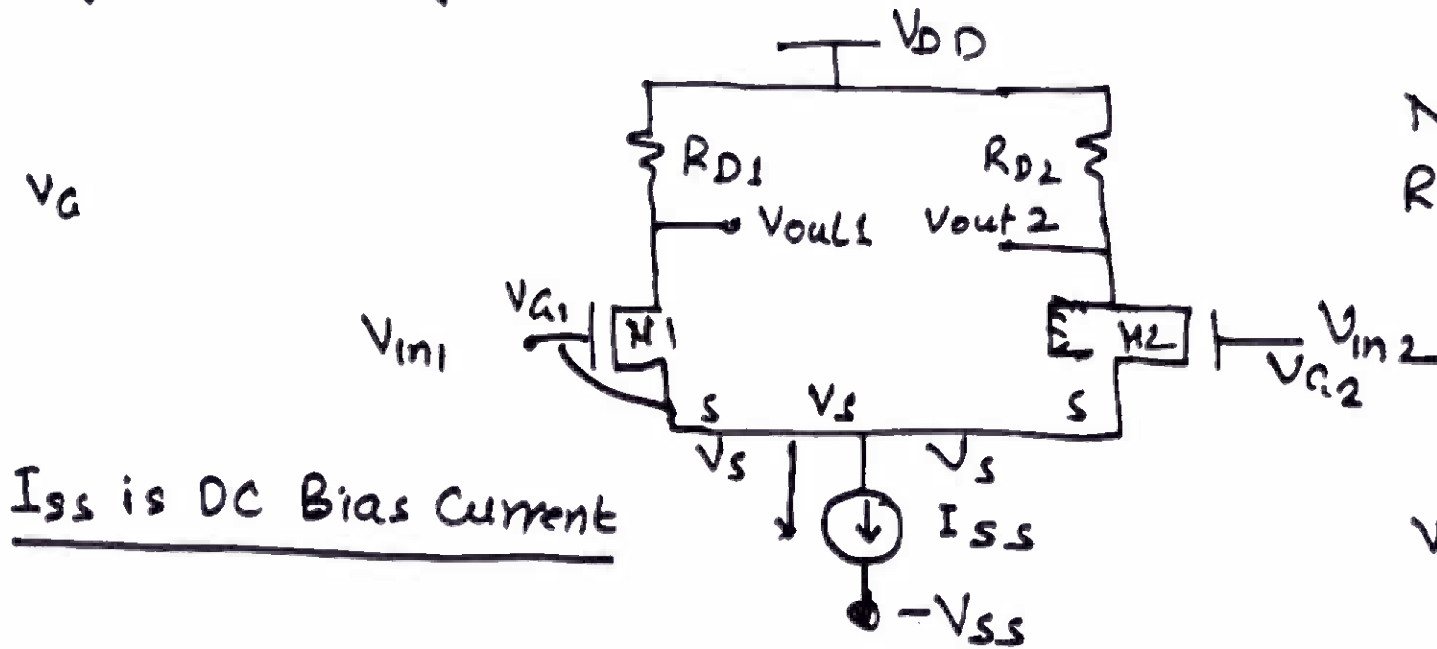


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A Diffamp consists of Two Single Ended Amplifier,

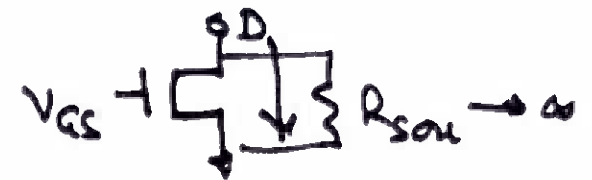


Modified Version:



$I_{SS}$  is DC Bias Current

Normally  
 $R_{D1} = R_{D2} = R_D$



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## Input Common Mode Range (ICMR)

$V_{CMmax}$  ,  $V_{CMmin}$  are two limits of Common Mode Inputs.

- ①  $V_{CMmax}$  occurs when  $M1$  &  $M2$  are at edge of 'saturation' i.e.  $V_{GS} - V_T = V_{DS}$

In our case it means

$$V_{CMmax} - V_T = V_{DD} - \frac{I_{SS}}{2} R_D$$

$$\therefore V_{CMmax} = V_T + V_{DD} - \frac{I_{SS}}{2} R_D.$$



Clearly  $V_{id} = V_{GS1} - V_{GS2}$

If  $V_{id}$  is +tive  $V_{GS1} > V_{GS2}$

$\therefore i_{DS1} > i_{DS2}$

But if  $V_{id}$  is +ve  $V_{GS2} > V_{GS1}$

& then  $i_{DS2} > i_{DS1}$

It is possible that at a value of  $V_{id}$ , only one transistor is ON (Saturated) and the other OFF.

$\therefore$  All the  $I_{SS}$  flows through 'ON' transistor =  $i_{DS1}$  (say)

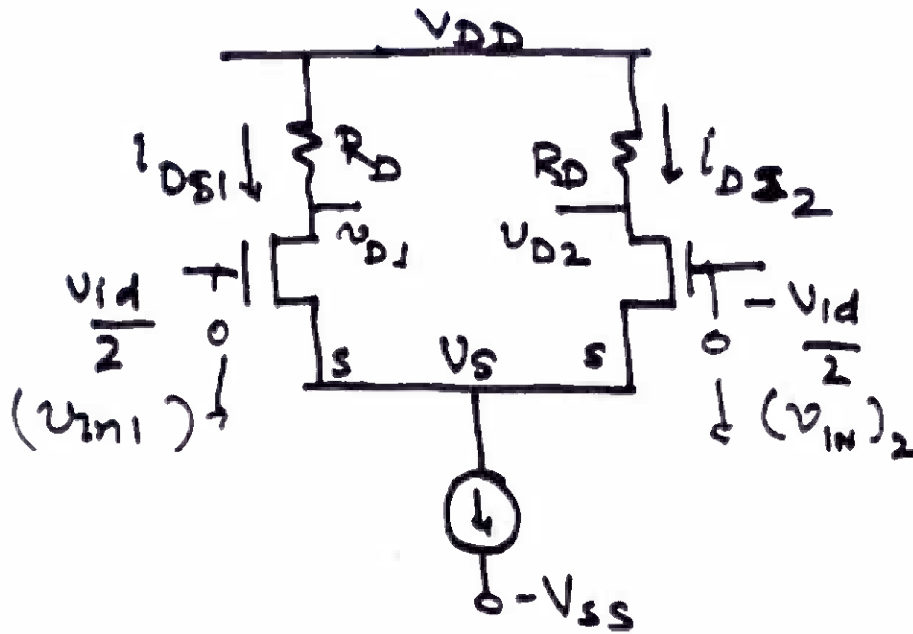
Then  $i_{DS1} = I_{SS} = \frac{\beta_n}{2} \left(\frac{W}{L}\right) [V_{GS1} - V_T]^2 \quad (\lambda = 0)$



# Differential Mode Input Response

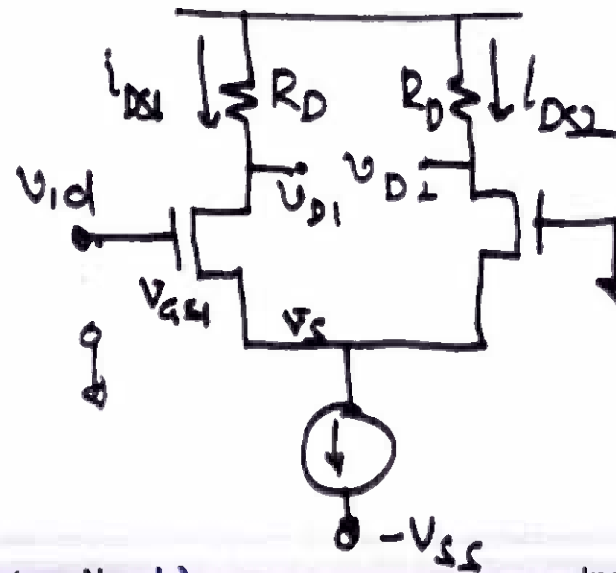


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$$\begin{aligned}
 v_{id} &= v_{in1} - v_{in2} \\
 &= \frac{v_{id}}{2} - \left(-\frac{v_{id}}{2}\right) = v_{id}
 \end{aligned}$$

Alternatively →





$$\text{OR } V_{GS1} = V_T + \sqrt{2I_{SS} / [\beta'_n (W/L)]}$$

We define  $V_{OV}$  (over voltage)

$$= \sqrt{2 \frac{I_{SS}}{2} / \beta'_n (W/L)}$$

where  $V_{OV}$  is for  $\frac{I_{SS}}{2}$

$$\therefore V_{GS1} = V_T + V_{OV}(\sqrt{2})$$

$$\therefore V_{id \max} = V_{GS1} + V_S$$

But min  $V_S$  can be obtained from  $M_2$  as  $V_{GS2} = 0$

$$\therefore V_{in2} = 0 - V_S \quad \text{or} \quad V_S = 0 - V_T = -V_T$$

$$\therefore V_{id \max} = V_T + \sqrt{2}V_{OV} - V_T = \sqrt{2}V_{OV}$$



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## Differential with Common Input Voltage

$$v_{in1} = v_{in2} = v_{a1} = v_{a2}$$

As source is common to both sides

$$v_{g1} = v_{g2} = v_{gs1} = v_{gs2}$$

$$\text{Then } v_{cm} = v_{in1} = v_{in2} = v_{gs1} = v_{gs2}$$

$$\therefore v_s = v_{cm} - v_{gs}$$

But we  
have

$$v_{ov} = v_{gs} - v_E$$

Since  $v_{gs1} = v_{gs2}$ , hence M1 & M2 will have

SAME current. That  $i_{D1} = i_{D2}$

$$\text{But } i_{D1} + i_{D2} = I_{SS}$$



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$$\therefore I_{D1} = I_{D2} = \frac{I_{SS}}{2}$$

$$\therefore I_{D1} = I_{D2} = \frac{I_{SS}}{2} = \frac{1}{2} \frac{\beta_n'(W/L)}{2} (V_{GS}' - V_T)^2$$

One Trans

$$\approx \frac{I_{SS}}{2} = \frac{\beta_n'(W/L)}{4} (V_{OV})^2$$

$$\therefore V_{OV} = \sqrt{\frac{2 I_{SS}}{\beta_n'(W/L)}}$$

$$\text{Then } V_{D1} = V_{D2} = V_{O1} = V_{O2} = V_{DD} - \frac{I_{SS}}{2} \cdot R_D$$

$$\text{Then } \underline{V_{O1} - V_{O2} = 0}$$

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Hence  $V_0 = V_{01} - V_{02} = 0$

and input =  $V_{CM}$

$\therefore$  Common Mode Voltage Gain

$$A_{CM} = \frac{V_0}{V_{CM}} = 0$$

$$CMRR = \frac{A_{dm}}{A_{CM}} \quad (80 \text{ dB} - 120 \text{ dB})$$

Assumption here was,  $M_1$  &  $M_2$  are identical i.e.

$$V_{T1} = V_{T2}$$

$I_{SS}$  is ideal current source.

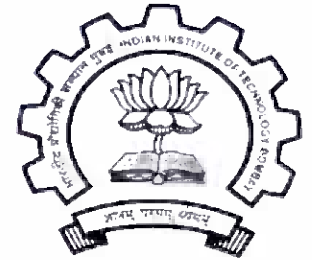
and  $R_{D1} = R_{D2} = R_D$

Thus any Noise overriding both  $V_{in1}$  &  $V_{in2}$  will act like Common Mode Signal & Gets Rejected at the Output

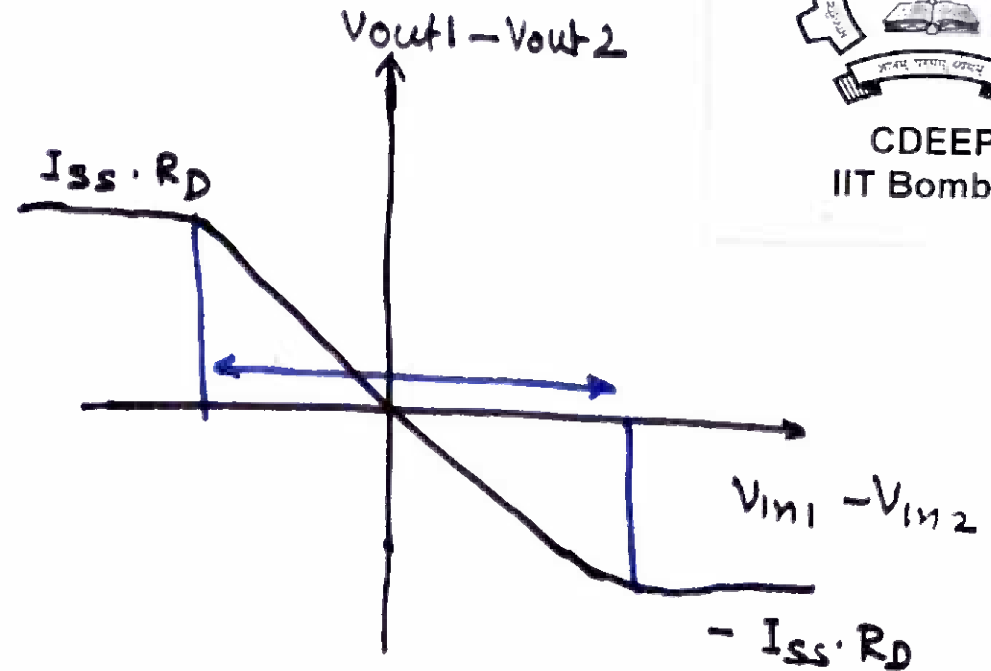
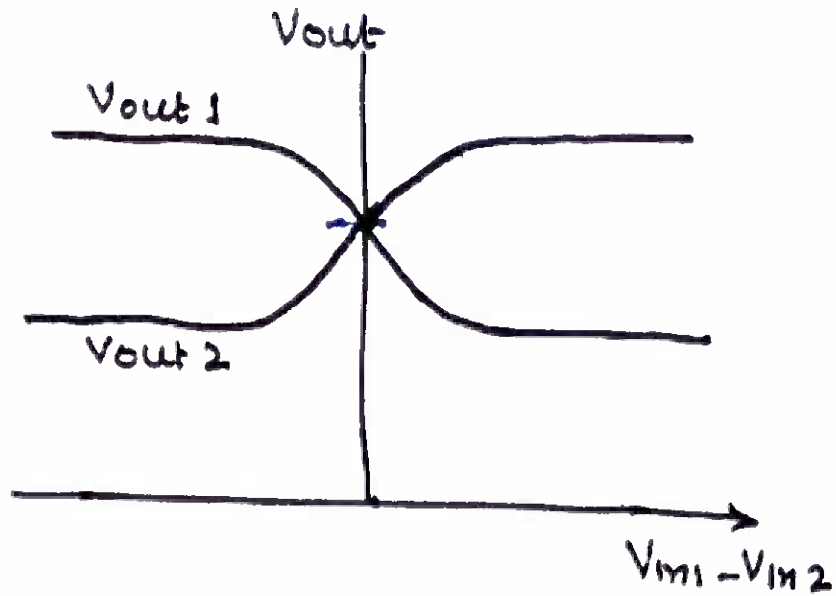


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## I/O Characteristics



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$$V_{out1} = V_{DD} - I_{SS} R_D$$

$$V_{out2} = V_{DD} - I_{SS} R_D$$

when  $V_{in1} - V_{in2}$  swings from  $-\infty$  to  $+\infty$

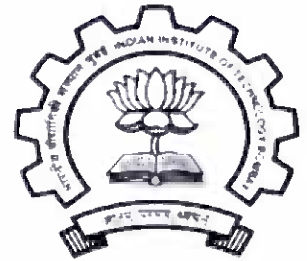
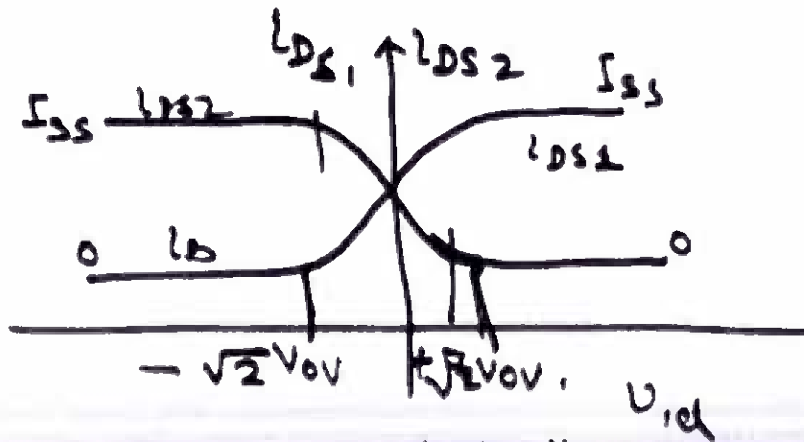
By complementary argument

$$V_{idmin} = -\sqrt{2}V_{ov}$$

$\therefore$  Currents in  $M_1$  &  $M_2$  goes from Max to Min in the range

$$-\sqrt{2}V_{ov} \leq V_{id} \leq \sqrt{2}V_{ov}$$

At  $V_{id}/2$ , Both  $M_1$  &  $M_2$  carries  $I_{SS}/2$  current



## Large signal operation

For the Diffamp under review

$$V_{id} = V_{GS1} - V_{GS2} = V_{G1} - V_{G2}$$

In choice of Biasing, we assure that  $M_1$  &  $M_2$  when 'ON' are in Saturation.

We have

$$i_{DS1} = \frac{\beta_n'}{2} (V_{GS1} - V_T)^2 (W/L) \quad \text{--- (i)} \quad (\lambda = 0)$$

$$i_{DS2} = \frac{\beta_n'}{2} (W/L) (V_{GS2} - V_T)^2 \quad \text{--- (ii)}$$

or

$$\sqrt{i_{DS1}} = \sqrt{\frac{\beta_n'}{2} (W/L)} (V_{GS1} - V_T) \quad \text{--- (iii)}$$

$$\sqrt{i_{DS2}} = \sqrt{\frac{\beta_n'}{2} (W/L)} (V_{GS2} - V_T) \quad \text{--- (iv)}$$



$$\text{However } v_{GS1} - v_{GS2} = v_{id} \\ = v_{G1} - v_{G2}$$

$$\therefore \sqrt{i_{DS1}} - \sqrt{i_{DS2}} = \sqrt{\frac{\beta_n (W/L)}{2}} v_{id} \quad \text{--- (vi)}$$

$$\text{However } i_{DS1} + i_{DS2} = I_{SS} \quad \text{--- (vii)}$$

Squaring (vi)

$$\left[ \sqrt{i_{DS1}} - \sqrt{i_{DS2}} \right]^2 = \frac{\beta_n}{2} v_{id}^2$$

$$\text{or } \underline{i_{DS1} + i_{DS2}} - 2\sqrt{i_{DS1} i_{DS2}} = \frac{\beta_n}{2} v_{id}^2$$

$$\text{or } I_{SS} - 2\sqrt{i_{DS1} i_{DS2}} = \frac{\beta_n}{2} (v_{id})^2$$



Solving

$$i_{DS1} = \frac{I_{SS}}{2} \pm \sqrt{\beta_n I_{SS}} \left( \frac{V_{id}}{2} \right) \left[ 1 - \frac{(V_{id}/2)^2}{I_{SS}/\beta_n} \right]$$

$$\therefore i_{DS1} = \frac{I_{SS}}{2} + \sqrt{\beta_n I_{SS}} \left[ 1 - \frac{(V_{id}/2)^2}{I_{SS}/\beta_n} \right] \left( \frac{V_{id}}{2} \right)$$

$$\therefore i_{DS2} = \frac{I_{SS}}{2} - \sqrt{\beta_n I_{SS}} \left[ 1 - \frac{(V_{id}/2)^2}{I_{SS}/\beta_n} \right] \left( \frac{V_{id}}{2} \right)$$

(Please note  $i_{DS1} + i_{DS2} = I_{SS}$ )

At  $V_{id} = 0$   $i_{DS1} = I_{SS}/2$

$i_{DS2} = I_{SS}/2$

& then  $V_{GS1} = V_{GS2} = V_{GS}$



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If  $V_{id}/V_{ov} < 1$

$$I_{DS1} = \frac{I_{SS}}{2} + \frac{I_{SS}}{V_{ov}} \left( \frac{V_{id}}{2} \right)$$

$$I_{DS2} = \frac{I_{SS}}{2} - \frac{I_{SS}}{V_{ov}} \left( \frac{V_{id}}{2} \right)$$

$\therefore I_{DS1} \propto V_{id}$  Linear operation

$\therefore$  Condition for Linear Operation is  $V_{id} \ll V_{ov}$





$$\begin{aligned} \text{Then } \frac{I_{SS}}{2} &= \frac{\beta_n}{2} \left(\frac{W}{L}\right) (V_{GS} - V_T)^2 \\ &= \frac{1}{2} \beta_n V_{OV}^2 \end{aligned}$$

$$\therefore \frac{I_{SS}}{\beta_n} = V_{OV}^2$$

$$\therefore i_{DS1} = \frac{I_{SS}}{2} + \frac{I_{SS}}{V_{OV}} \left(\frac{V_{id}}{2}\right) \sqrt{1 - \left(\frac{V_{id}/2}{V_{OV}}\right)^2}$$

$$i_{DS2} = \frac{I_{SS}}{2} - \frac{I_{SS}}{V_{OV}} \left(\frac{V_{id}}{2}\right) \sqrt{1 - \left(\frac{V_{id}/2}{V_{OV}}\right)^2}$$

Clearly at  $V_{id} = 2V_{OV}$

$$i_{DS1} = i_{DS2} = I_{SS}/2$$