PWM/PULSE Width modulation
The diagram shows an electronic circuit with three amplifiers labeled 1, 2, and 3, feeding into a voltage source labeled $V_0$. The graph on the right illustrates the temporal behavior of two voltages, $V_T$ and $V_D$, over time $t$. The graph highlights the minimum pulse width for $V_D$. The circuit and graph are labeled with $V_T$, $V_D$, and $V_0$. The graph also shows time intervals marked as $T_s$. The diagram includes a note indicating "minimum pulse width."