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EE 669 L 11 / Slide 01

Thermal Oxidation of Silicon

- (i) SiO_2 Structure & Properties
- (ii) Kinetics of Thermal Growth of Silicon
- (iii) Si- SiO_2 interface
- (iv) Characterisation of Quality of SiO_2
- (v) Growth Technology

MOS Technology is used in creation of Silicon ICs, owe most of it's credit to $\text{Si}-\text{SiO}_2$ system.

In fact more than calling ICs as Silicon ICs, we should really call ICs as SiO_2 ICs.

SiO_2 is used in VLSI Technology as

- (i) Field Oxide (4000 \AA to $1.4 \mu\text{m}$)
- (ii) Masking Oxide (600 \AA to 4000 \AA)
- (iii) Gate Oxide (Gate Dielectric) [1 nm to 1000 \AA]
- (iv) Pad Oxide (100 to 400 \AA)
- (v) Chemical oxide ; During RCA cleaning



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Technologies used for creation of SiO_2

1. Thermal Oxidation

(a) Dry Oxidation

(b) Wet Oxidation

2. Deposition techniques

(a) Chemical Vapour Deposition

(b) Physical Vapour Deposition

(c) Rapid Thermal Oxidation

(d) Sol.-gel Process

(e) Plasma Oxidation

Thermal Oxidation is most basic oxidation technique and is most important step in creation of Gate-Oxide in a MOS Transistor.

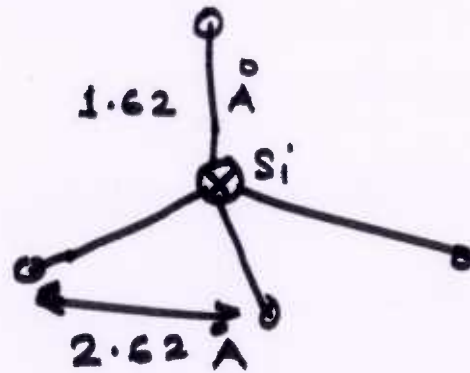
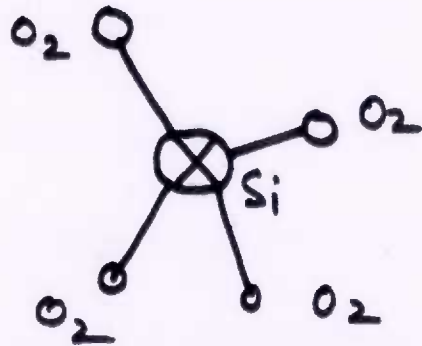


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Properties of Silicon Dioxide

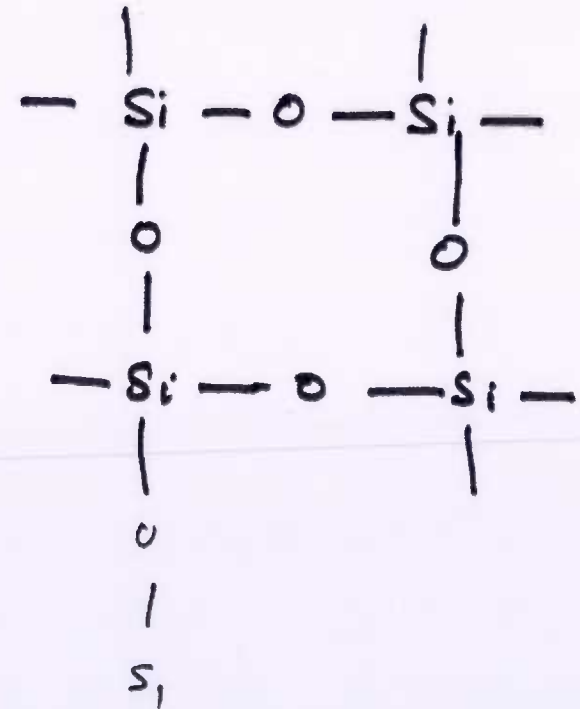
(i) Bonding : Si-O-Si is a natural bond in SiO_2 .



O-O Bond length $\rightarrow 2.62 \text{ \AA}$

Si-O Bond Length $\rightarrow 1.62 \text{ \AA}$

Si-Si Bond Length $\rightarrow 3 \text{ \AA}$



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SiO_2 is a Dielectric Material and is available in

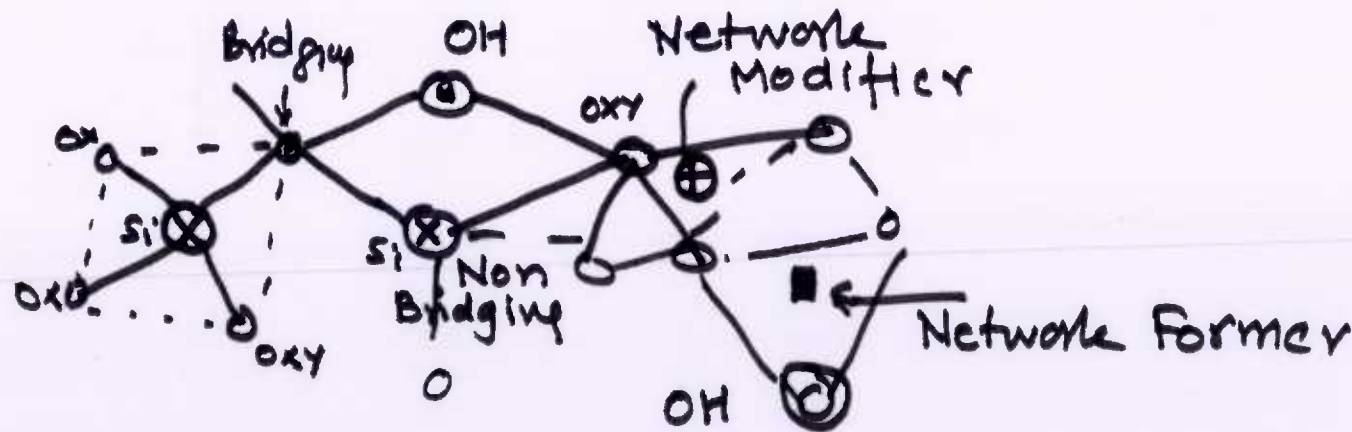
ci) Amorphous phase
cii) Crystalline phase \rightarrow Quartz



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Thermally grown oxides are Amorphous in nature. Typical atomic structure of SiO_2 with some impurities or ions is as below:



Some observations:

(i) From the lattice structures of SiO_2 , we observe that- if Si has to leave SiO_2 lattice, it must break 4. Si-O bonds.

While if Oxygen atom has to leave SiO_2 lattice, it ~~is~~ needs to break only One bond.

(ii) SiO_2 with no impurities like Diffusing Impurities, or Sodium, Lead, Barium etc, is called Intrinsic Silica.

While SiO_2 with impurities is called Extrinsic Silica.



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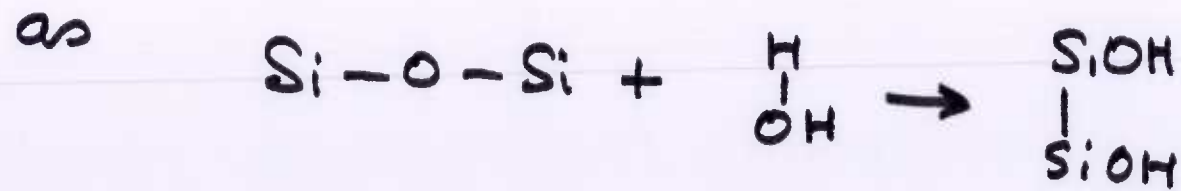
(iii) Boron, Phosphorus & Arsenic (etc.) replace silicon and hence create bond between oxygen & them. Such impurities are called Network former.



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However if impurities sit in interstitial sites, then they modify the network of SiO_2 , and hence called Network Modifier. Na_2O , Pb_2O_3 , Ba_2O_3 are such modifier.

(iv) If SiO_2 is put into water, we can have reaction



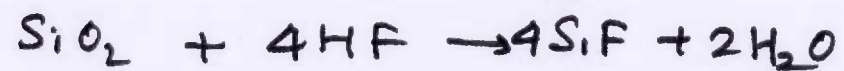
It is also to be noted that SiO_2 is strong hydrophilic in nature.

(v) Physical Properties of SiO_2

1. $\sigma \leq 10^{-16}$ mhos/cm
2. Bandgap $E_g \geq 9$ eV
3. Refractory Index $n = 1.45$
4. Density = 2.22
5. No. of atoms/cc = 2.3×10^{22} /cc
6. Dielectric Constant $K_{\text{SiO}_2} = 3.9$
7. Dielectric Strength = 10^7 V/cm.

(vi) SiO_2 has strong Utility in IC processing as

(a) Easy to Etch



(b) Excellent Insulator with High Dielectric Strength



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(c) Most Impurities (in Silicon) have poor Diffusion Constant in SiO_2 .

(d) It is extremely Stable.

(e) Interface is quite Stable ($\text{Si}-\text{SiO}_2$)

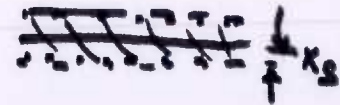
and very much reproducible as is desired in IC fabrication.

(vii) When Si is Oxidised, there is volume expansion.

If x_s is Silicon Thickness and after oxidation

it creates Oxide Thickness x_{ox} . Then we use

Law of Mass-action to say



$$N_s \cdot x_s = N_{ox} x_{ox}$$

where N_s is Si Conc = $5 \times 10^{22} / \text{cc}$

and N_{ox} is SiO_2 Conc. = $2.3 \times 10^{22} / \text{cc}$

$$\therefore x_s = \frac{N_{ox}}{N_s} \cdot x_{ox} = 0.46 x_{ox}$$

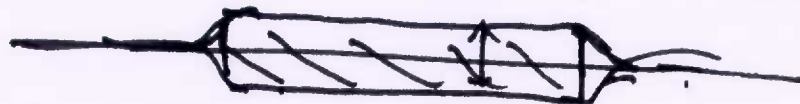


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Local Oxidation Silicon.

FOX

LOCOS



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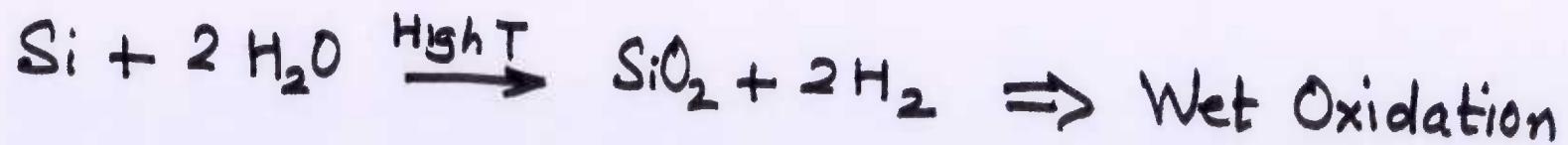
"Kinetics of Thermal Oxidation of Silicon"

Silicon dioxide (SiO_2) is generally grown by oxidation of Silicon at high temperatures (800°C to 1200°C) in Oxygen rich ambient.

It could be pure O_2 or H_2O . Oxygen reacts with Silicon to form SiO_2 .



SiO_2 formation by oxidation of Silicon in Water Vapour may have a bit complicated reaction. Overall however it may be like:



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