1. Which of the following instruction perform as of indirect RAM to accumulator?
   a) MOV A, Rn  b) MOV @Ri, A  c) MOV A, @Ri  d) MOV Rn, A
2. ACALL instruction allows specifying ______ address in the instruction and calling subroutine within ______ program memory block.
   a) 2byte, 3K  b) 11bit, 2K  c) 9bit, 2K  d) 1byte, 3K
3. Which of the following instruction perform the move accumulator to external RAM of 16bit address?
   a) MOV @ DPTR, A  b) MOVX @ Ri, A  c) MOV A, @ Ri  d) MOVX @ DPTR, A
4. Which of the following instruction perform jump indirect relative to DPTR
   a) JMP A+DPTR  b) JMP DPTR  c) JMP @A+DPTR  d) SJMP A+DPTR
5. Which of the following instruction is wrong
   a) INC DPTR  b) MOV @DPTR, A  c) MOV A, @A+DPTR  d) DEC DPTR
6. Which of the following instruction is of logical instructions?
   i) CPL A  ii) JC rel  iii) DA A  iv) ANL A, Rn  v) RR A  vi) CPL bit
   a) i, v  b) v, iii, I  c) iv, ii  d) v, iii, ii
7. What instruction performs Compare immediate to indirect and jump if not equal.
   a) CJNE A, #data, rel  b) CJNE Rn, #data, rel  c) CJNE @ Ri, #data, rel
8. What is the Result of \( \text{RR A} \) instruction if accumulator contains 1000 0000.
   a) 0000 0001  b) 0000 0000  c) 0100 0000  d) 0000 0010
9. MOV 45, #4FH  MOV R0, 45H  INC R0  MOV @R0, #30H
   What is the location value of R0 and the content at that place?
   a) 45H, 4F b) 50H, 30H  c) 30H, 50H  d) 50H, 45H
10. Which of the following is of type memory initialized Directive?
    i) DS  ii) SET iii) DW  iv) DBIT
    a) i, iii  b) ii  c) iii  d) iv, ii
11. Which of the following is not a program linking directive
    i) EXTRN  ii) SEGMENT  iii) NAME  iv) PUBLIC  v) USING
    a) iv, v  b) ii, iii  c) i, iii  d) ii, v
12. SP of 8051 is of ___ wide and it is loaded with the default value of ___ after reset.
    a) 2 byte, 08H  b) 8 bit, 07H  c) 1 byte, 09H  d) 8 bit, 06H
13. Which of the following instruction is used to set bit port directly?
    a) SET P1.0  b) MOV P1.0, bit  c) SETB P1.0  d) JB P1.0, bit
14. MOV A, #56H  MOV R1, #50H
MOV 50H, #45H
XCHD A, @R1
What is the result at A, R1?
a) 56H, 45H  b) 45H, 50H  c) 50H, 56H  d) 45H, 56H
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