

1. The contents of different registers are given below. Form **Effective addresses** for different addressing modes are as follow :  
Offset = 5000H  
[AX]- 1000H, [BX]- 2000H, [SI]- 3000H, [DI]- 4000H, [BP]- 5000H,  
[SP]- 6000H, [CS]- 0000H, [DS]- 1000H, [SS]- 2000H, [IP]- 7000H.
  - I. MOV AX, [5000H]
    - a) 5000H b) 15000H c) 10500H
  - II. MOV AX, [BX] [SI]
    - a) 13000H b) 15000H c) 12000H
  - III. MOV AX, 5000H [BX] [SI]
    - a) 20000H b) 1A000H c) 1A00H
2. The conditional branch instruction JNS performs the operations when if \_\_\_
  - a) ZF =0 b) SF=0 c) PF=0 d) CF=0
3. Vector address of TRAP
  - a) 24H b) 36H c) 24 d) 18H
4. SOD pin can drive a D flip-flop?
  - a) SOD cannot drive any flip-flops.
  - b) SOD cannot drive D flip-flop, but can drive any other flop-flops.
  - c) Yes, SOD can drive D flop-flop.
  - d) No, SOD cannot drive any other flop-flops except D flop-flop.
5. IDIV and DIV instructions perform the same operations for?
  - a) Unsigned number b) Signed number c) Signed number & Unsigned number d) none of above.
6. What is the output of the following code  
AL=88 BCD, CL=49 BCD  
ADD AL, CL  
DAA
  - a) D7, CF=1 b) 37, CF=1 c) 73, CF=1 d) 7D, CF=1
7. What is the output of the following code  
AL= 49 BCD, BH= 72 BCD  
SUB AL, BH  
DAS
  - a) AL=D7, CF=1. b) AL=7D, CF=1. c) AL=77, CF=1 d) none of them.
8. What is the output of the following code  
AL= -28 decimal, BL=59 decimal  
IMUL BL  
AX=?, MSB=?
  - a) AX= F98CH, MSB=1. b) AX= 1652, MSB=1. c) BX F9C8H, MSB=1. d) BX= 1652, MSB=1.
9. What is the output of the following code  
AL= 00110100 BL= 00111000  
ADD AL, BL  
AAA
  - a) AL = 6CH b) 12H c) 12 d) C6H
10. What is the output of the following code  
AL=00110101 BL= 39H

SUB AL, BL

AAS

a) AL= 00000100, CF=1 b) BL=00000100, CF=0 c) AL=11111100 CF=1 d) BL=00000100, CF=1

11. What is the output of the following code

CF =0, BH = 179

RCL BH, 1

a) CF=0, OF= 1, BH= 01100101 b) CF=1, OF=1, BH=01100110

c) CF=1, OF =0, BH= 01001101 d) CF=0, OF=0, BH=00101100

12. What is the output of the following code

SI=10010011 10101101, CF=0

SHR SI, 1

a) 37805, CF=1, OF=1 b) 18902, CF=1, OF=1

c) 19820, CF=1, OF=1 c) 53708, CF=1, OF=1

13. What is the output of the following code

BX=23763CL=8

ROL BX, CL

a) 0101110011010011, CF=0

b) 1101001101011100, CF=0

c) 0110100010011101, CF=1

c) 1011100110001100, CF=1

14. What is the output of the following code

PUSH AL

a) Decrement SP by 2 & push a word to stack

b) Increment SP by 2 & push a word to stack

c) Decrement SP by 2 & push a AL to stack

d) Illegal

15. What is the output of the following code

AX = 37D7H, BH = 151 decimal

DIV BH

a) AL = 65H, AH= 94 decimal

b) AL= 5EH, AH= 101 decimal

c) AH= E5H, AL= 5EH

d) AL= 56H, AH= 5EH

16. In 8086 microprocessor one of the following instructions is executed before an arithmetic operation

a) AAM b) AAD c) DAS d) DAA

**Key:**

2.1 (I) B	(II) C	(III) B	2.2 B	2.3 A	2.4 C	2.5 B
2.6 B	2.7 C		2.8 A	2.9 C	2.10 A	2.11 B
2.12 B	2.13 B		2.14 D	2.15 B	2.16 B	